Altera High Definition Multimedia Interface Ip Core User Guide

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Altera High Definition Multimedia Interface
The Altera High-Definition Multimedia Interface (HDMI) IP core provides support for next generation video display interface technology. The HDMI standard specifies a digital communications interface for use in both internal and external connections: • Internal connections—interface within a PC and monitor

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High-Definition Multimedia Interface (HDMI) IP Core User Guide
The Altera High-Definition Multimedia Interface (HDMI) IP core provides support for next-generation video display interface technology. Release Information Version 14.1 Release December 2014 Ordering Code IP-HDMI Product ID 0121 Vendor ID 6AF7 ©2014 Altera Corporation.

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Altera High-Definition Multimedia Interface User Guide Errata for HDMI IP Core in the Knowledge Base . 1.12. HDMI IP Core v15.1 Table 13. v15.1 November 2015; Description Impact Added the following new GUI parameters: HDMI source. Support for 8-channel audio; Support for deep color ...

High-Definition Multimedia Interface (HDMI) Intel FPGA IP ...
This reference design demonstrates the Intel FPGA High Definition Multimedia Interface (HDMI) 2.0 video connectivity IP core with a video processing pipeline based on IP cores from the Intel FPGA Video and Image Processing (VIP) Suite. This design is intended as a simple reference for interconnectivity between the HDMI IP core and the VIP Suite. Additionally, this design demonstrates the use of separate clocks for the RX and TX HDMI IP cores, allowing for differing RX and TX video resolutions.

Arria 10 HDMI 4Kp60 with Video and Image Processing ...
The converted image is mixed and displayed over user-selectable output such as SDI, DVI, or high-definition multimedia interface (HDMI). The design uses Altera's intellectual property (IP) for SDI and video and image processing (VIP) functions.

High Definition Video (UDX) Design Example | Design Store ...
One I/O expansion slot—one high-speed mezzanine card (HSMC) connector 256 MB of SDRAM memory High-definition multimedia interface (HDMI) and serial digital interface (SDI) connections SMAs : Version: 1.0: Family: Arria V: Device: 5AGXFB3: Documentation

Arria V GX Starter Kit | Design Store for Intel® FPGAs
Core Features • Conforms to the High-Definition Multimedia Interface (HDMI) Specification versions 1.4, 2.0b, and 2.1 • Supports transmitter and receiver on a single device transceiver quad • Supports pixel frequency up to 600 MHz for HDMI 2.0 and 1,200 MHz for HDMI 2.1 • Supports fixed rate link (FRL) for HDMI 2.1

HDMI Intel® FPGA IP User Guide
Cyclone 10 GX HDMI 4Kp60 with Video and Image Processing Pipeline reference design demonstrates the Intel FPGA High Definition Multimedia Interface (HDMI) 2.0 video connectivity IP core with a video processing pipeline based on IP cores from the Intel FPGA Video and Image Processing (VIP) Suite.

Cyclone 10 GX HDMI 4Kp60 with Video and Image ... - Intel
The Altera® MAX® 10 FPGA Development Kit provides a full featured design platform built around a 50 K logic elements (LEs) MAX 10 FPGA, optimized for system level integration with on-die analog-to-digital converter (ADC), dual-configuration flash, and DDR3 memory interface support.

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The Altera ® MAX ® 10 FPGA Development Kit provides a full featured design platform built around a 50 K logic elements (LEs) MAX 10 FPGA, optimized for system level integration with on-die analog-to-digital converter (ADC), dual-configuration flash, and DDR3 memory interface support.

Terasic - All FPGA Boards - MAX 10 - MAX 10 FPGA ...
HDMI Tx-Only VIP Suite Design for Arria 10. Description. This reference design demonstrates the Intel FPGA High Definition Multimedia Interface (HDMI) 2.0 video connectivity IP core with a video processing pipeline based on IP cores from the Intel FPGA Video and Image Processing (VIP) Suite.

HDMI Tx-Only VIP Suite Design for Arria 10 | Design Store ...
The Altera ® Arria ® V GX Starter Kit provides a complete design environment that includes all the hardware and software you need to develop cost-sensitive FPGA applications immediately. The development kit is RoHS compliant. The development kit features the following: Arria V GX FPGA—360KLE, F1152 package, 24X6.5G XCVRs, C4 speed grade

Terasic - Phased Out - Main Boards - Altera Arria V GX ...
HDMI (High-Definition Multimedia Interface) is an audio/video interface for transmitting uncompressed video data and compressed or uncompressed digital audio data from an HDMI-compatible source device, such as a display controller, a compatible computer monitor, video projector, digital television, or digital audio device.

HDMI Output Example Design using Vivado for Mimas A7 FPGA ...
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