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John has been involved in USB since its inception and wrote his first USB Design By Example book in 1999. He works with a wide variety of USB folk in architecture, design and debug of their embedded products. He has written five "How-To" books and is now working on an update to his SuperSpeed book and a new

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USB Type-C Design By Example book.

SuperSpeed Device Design By Example: Hyde, John ...

SuperSpeed Device Design By Example, by John Hyde, is the latest in a series of "How-To" USB books. This guide takes a practical approach to designing and implementing SuperSpeed USB peripherals. Starting with the essential theory on USB 3.0, it provides a series of worked examples using the FX3 SuperSpeed Explorer Kit. Accelerate Your Design

SuperSpeed Device Design By Example - By John Hyde

SuperSpeed Device Design By Example. This is a "How-To" book which explains, with hands-on examples, how to design and implement a SuperSpeed USB peripheral that can interface to your hardware using a 32-bit 100MHz bus with standard or custom protocols.

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SuperSpeed Device Design By Example by John Hyde

SuperSpeed Design Examples V1.2 From John Hyde's book - Superspeed Device Design by Example. This book is really great, but all Visual Studio projects were a little out-of-date, so I retarget/recompile with Visual Studio 2017 Community on Windows 10 (with some fixes to get rid of compile errors). The rest (FX3 firmware & CLPD) are the same.

GitHub - haizzus/SuperSpeed-Design-Examples-V1.2 ...

SuperSpeed Device Design By Example, by John Hyde, is the latest in a series of "How-To" USB books. This guide takes a practical approach to designing and implementing SuperSpeed USB peripherals. Starting with the essential theory on USB 3.0, it provides a series of worked examples

Superspeed Device Design By Example

SuperSpeed Device Design By Example, by John Hyde, is the

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latest in a series of "How-To" USB books. This guide takes a practical approach to designing and implementing SuperSpeed USB peripherals. Starting with the essential theory on USB 3.0, it provides a series of worked examples using the FX3 SuperSpeed Explorer Kit.

Superspeed Device Design By Example

SuperSpeed Device Design By Example by John Hyde. English | ISBN: 1500588059 | 300 pages | EPUB | September 1, 2014 | 14 Mb. This is a "How-To" book which explains, with hands-on examples, how to design and implement a SuperSpeed USB peripheral that can interface to your hardware using a 32-bit 100MHz bus with standard or custom protocols.

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USB SuperSpeed Code Examples - Cypress Semiconductor

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tackle short-term projects that others may deem 'too-difficult'.

USB Design By Example by John Hyde

Synopsis. About this title. This is a "How-To" book which explains, with hands-on examples, how to design and implement a SuperSpeed USB peripheral that can interface to your hardware using a 32-bit 100MHz bus with standard or custom protocols. The book is based on the Cypress FX3 SuperSpeed Device and the firmware examples are written around a low-cost SuperSpeed Explorer board and a companion CPLD board which are available from www.cypress.com/fx3book.

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The software examples are written for the Windows operating system and the CPLD examples are written in Verilog. The source code for all of the examples is downloadable from the

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