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**Blaine Readler - Verilog by Example, A Concise ...**  
• "Verilog by Example - A concise introduction for FPGA Design" by Blaine C. Readler, 2011, Full Arc Press 978-0-9834973-0-1 • "Starters Guide to Verilog 2001" by Ciletti, 2004, Prentice Hall 0-13-141556-5 • "Fundamentals of Digital Logic with Verilog Design" by Brown and Vranesic, 2003, McGraw-Hill, 0-07-282878-7

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The verilog is a bit out of date although he author does explain that newer versions have some differences. The first chapter on IN and OUT is pretty good as an introduction, several examples of combinatorial logic (although not really developing useful example).